

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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FEB 11 2002  
PATENT & TRADEMARK OFFICE  
In re Patent Application of )  
Gary N. Lai, et al. ) Group Art Unit: unknown  
Application No.: 09/955,913 ) Examiner: to be assigned  
Filed: September 18, 2001 )  
For: MULTIPLIER UNIT IN RECONFIGURABLE )  
CHIP )

**INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

**Non-Patent Literature Documents**

Chameleon Systems, Inc., Product Overview sheet for CS2000 Reconfigurable Communications Processor, © 2000

Chameleon Systems, Inc., Family Product Brief for CS2000 Reconfigurable Communications Processor, Advance Product Information, © 2000.

GLASKOWSKY, "Chameleon Crosses CENTRAL PROCESSING UNIT, FPGA," Microdesign Resources, June 12, 2000.

Dave Bursky [Editor-in-Chief], "Comm Processor Adjusts For Task At Hand," Electronic Design, May 15, 2000 pg. 66 et seq.

Graham Mostyn, "Benefits of Reconfigurable Processors for Communication System Design," Communication Systems Design, January 2000.

"Reconfigurables hit comms processing," Electronic Engineering Times, May 22, 2000.

Crista Souza, "Semiconductors: Chameleon a quick-change artist," Electronic Buyers' News, May 22, 2000.

The documents are being submitted within 3 months of the filing or entry of the national stage of this application or before the first Office Action on the merits, whichever is later, therefore no fee or certification is required under 37 C.F.R. § 1.97(b).

To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

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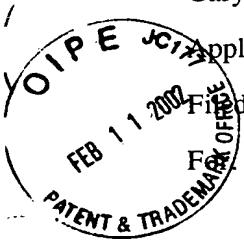
Date:

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Patent  
Attorney's Docket No. 032001-078

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of )  
Gary N. Lai, et al. ) Group Art Unit: unknown  
Application No.: 09/955,913 ) Examiner: to be assigned  
Filed: September 18, 2001 )  
FEDERAL TRADE COMMISSION  
DEMAND FOR  
MULTIPLIER UNIT IN RECONFIGURABLE  
CHIP )



**INFORMATION DISCLOSURE STATEMENT  
TRANSMITTAL LETTER**

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Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

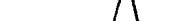
Enclosed is an Information Disclosure Statement and accompanying form PTO-1449 for the above-identified patent application.

- No additional fee for submission of an IDS is required.
- The fee of \$180.00 (126) as set forth in 37 C.F.R. § 1.17(p) is also enclosed.
- A certification under 37 C.F.R. § 1.97(e) is also enclosed.
- A certification under 37 C.F.R. § 1.97(e), and the fee of \$180.00 (126) as set forth in 37 C.F.R. § 1.17(p) are also enclosed.
- Charge \$ \_\_\_\_\_ to Deposit Account No. 02-4800 for the fee due.
- A check in the amount of \$ \_\_\_\_\_ is enclosed for the fee due.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

By: 

P.O. Box 1404  
Alexandria, Virginia 22313-1404

Date:

Substitute for form 1449A/PTO		ATTORNEY'S DKT No. 032001-078	APPLICATION No. 09/955,913
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		APPLICANT Gary N. Lai, et al.	
		FILING DATE September 18, 2001	GROUP unknown

<b>U.S. PATENT DOCUMENTS</b>					
Examiner Initials	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication (MM-DD-YYYY)	
	Number	Kind Code (if known)			
					

<b>FOREIGN PATENT DOCUMENTS</b>					
Examiner Initials	Foreign Patent Document		Country	Date of Publication (MM-DD-YYYY)	Translation
	Number	Kind Code (if known)			Yes

<b>NON PATENT LITERATURE DOCUMENTS</b>					
Examiner Initials	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				
	Chameleon Systems, Inc., Product Overview sheet for CS2000 Reconfigurable Communications Processor, © 2000				
	Chameleon Systems, Inc., Family Product Brief for CS2000 Reconfigurable Communications Processor, Advance Produce Information, © 2000				
	GLASKOWSKY, "Chameleon Crosses CENTRAL PROCESSING UNIT, FPGA," Microdesign Resources, June 12, 2000.				
	Dave Bursky [Editor-in-Chief], "Comm Processor Adjusts For Task At Hand," Electronic Design, May 15, 2000 pg. 66 et seq.				
	Graham Mostyn, "Benefits of Reconfigurable Processors for Communication System Design," Communication Systems Design, January 2000				
	"Reconfigurables hit comms processing," Electronic Engineering Times, May 22, 2000				
	Crista Souza, "Semiconductors: Chameleon a quick-change artist," Electronic Buyers' News, May 22, 2000				
Examiner Signature			Date Considered		

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. SEND TO: Assistant Commissioner for Patents, Washington, D.C. 20231.